

565555

TIMERS

555556

sinnetics

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LINEAR INTEGRATED CIRCUITS

DESCRIPTION

The NE/SE 555 monolithic timing circuit is a highly stable controller capable of producing accurate time delays, or oscillation. Additional terminals are provided for triggering or resetting if desired. In the time delay mode of operation, the time is precisely controlled by one external resistor and capacitor. For a stable operation as an oscillator, the free running frequency and the duty cycle are both accurately controlled with two external resistors and one capacitor. The circuit may be triggered and reset on falling waveforms, and the output structure can source or sink up to 200mA or drive TTL circuits.

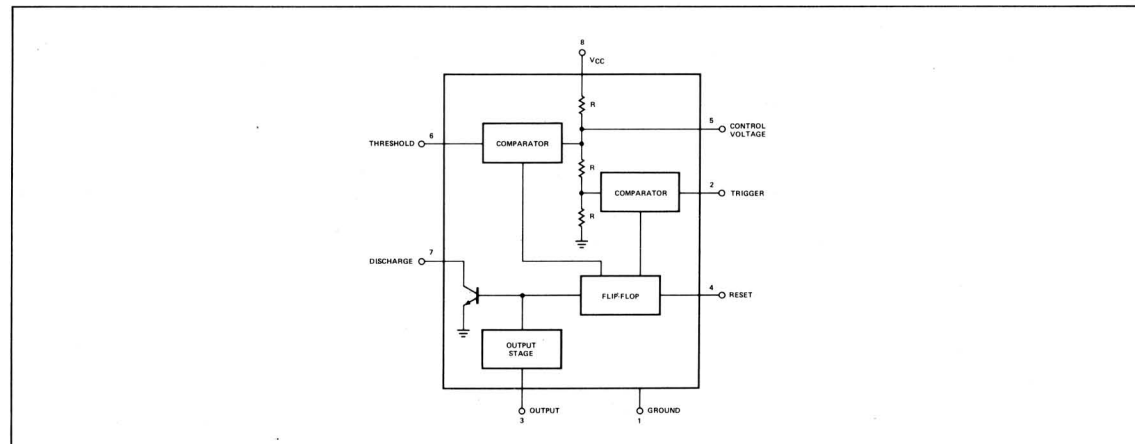
FEATURES

- TIMING FROM MICROSECONDS THROUGH HOURS
- OPERATES IN BOTH ASTABLE AND MONOSTABLE MODES
- ADJUSTABLE DUTY CYCLE
- HIGH CURRENT OUTPUT CAN SOURCE OR SINK 200mA
- OUTPUT CAN DRIVE TTL
- TEMPERATURE STABILITY OF 0.05% PER °C
- NORMALLY ON AND NORMALLY OFF OUTPUT

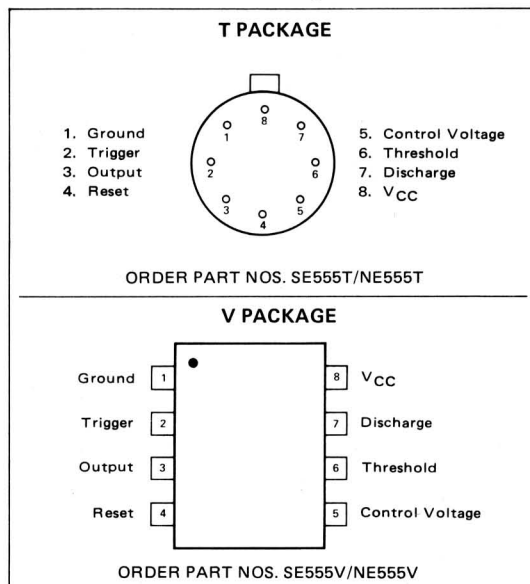
APPLICATIONS

PRECISION TIMING
PULSE GENERATION
SEQUENTIAL TIMING
TIME DELAY GENERATION
PULSE WIDTH MODULATION
PULSE POSITION MODULATION
MISSING PULSE DETECTOR

BLOCK DIAGRAM



PIN CONFIGURATIONS (Top View)



ABSOLUTE MAXIMUM RATINGS

Supply Voltage	+18V
Power Dissipation	600 mW
Operating Temperature Range	
NE555	0°C to +70°C
SE555	-55°C to +125°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 60 seconds)	+300°C

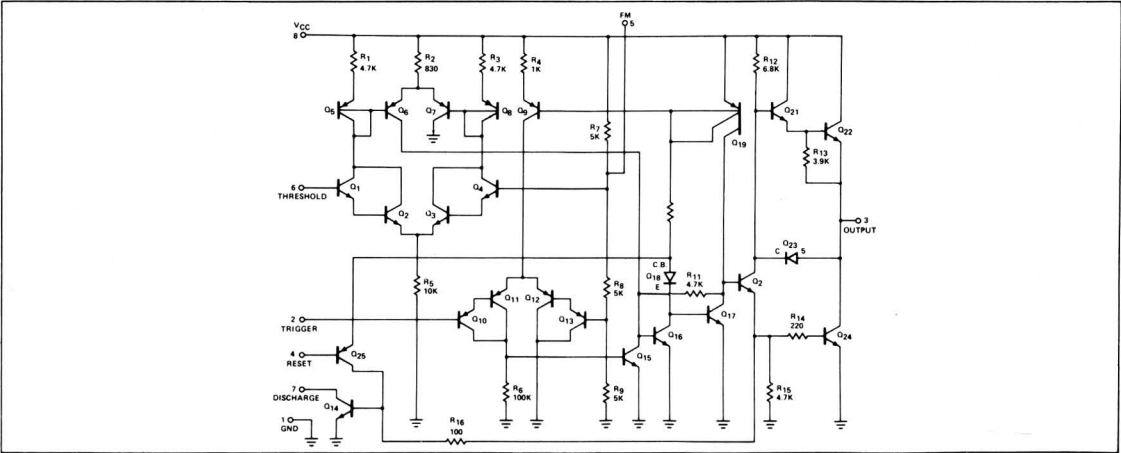
ELECTRICAL CHARACTERISTICS $T_A = 25^{\circ}\text{C}$, $V_{CC} = +5\text{V}$ to $+15$ unless otherwise specified

PARAMETER	TEST CONDITIONS	SE 555			NE 555			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
Supply Voltage		4.5		18	4.5		16	V
Supply Current	$V_{CC} = 5\text{V}$ $R_L = \infty$		3	5		3	6	mA
	$V_{CC} = 15\text{V}$ $R_L = \infty$		10	12		10	15	mA
Timing Error	Low State, Note 1							
Initial Accuracy	$R_A, R_B = 1\text{K}\Omega$ to $100\text{K}\Omega$		0.5	2		1		%
Drift with Temperature	$C = 0.1\text{ }\mu\text{F}$ Note 2		30	100		50		ppm/ $^{\circ}\text{C}$
Drift with Supply Voltage			0.05	0.2		0.1		%/Volt
Threshold Voltage			2/3			2/3		$\times V_{CC}$
Trigger Voltage	$V_{CC} = 15\text{V}$	4.8	5	5.2		5		V
	$V_{CC} = 5\text{V}$	1.45	1.67	1.9		1.67		V
Trigger Current			0.5			0.5		μA
Reset Voltage		0.4	0.7	1.0	0.4	0.7	1.0	V
Reset Current			0.1			0.1		mA
Threshold Current	Note 3		0.1	.25		0.1	.25	μA
Control Voltage Level	$V_{CC} = 15\text{V}$	9.6	10	10.4	9.0	10	11	V
	$V_{CC} = 5\text{V}$	2.9	3.33	3.8	2.6	3.33	4	V
Output Voltage Drop (low)	$V_{CC} = 15\text{V}$							
	$I_{\text{SINK}} = 10\text{mA}$		0.1	0.15		0.1	.25	V
	$I_{\text{SINK}} = 50\text{mA}$		0.4	0.5		0.4	.75	V
	$I_{\text{SINK}} = 100\text{mA}$		2.0	2.2		2.0	2.5	V
	$I_{\text{SINK}} = 200\text{mA}$		2.5			2.5		V
	$V_{CC} = 5\text{V}$							
Output Voltage Drop (high)	$I_{\text{SINK}} = 8\text{mA}$		0.1	0.25				V
	$I_{\text{SINK}} = 5\text{mA}$					.25	.35	
	$I_{\text{SOURCE}} = 200\text{mA}$		12.5			12.5		
	$V_{CC} = 15\text{V}$							
	$I_{\text{SOURCE}} = 100\text{mA}$							
	$V_{CC} = 15\text{V}$	13.0	13.3		12.75	13.3		V
Rise Time of Output	$V_{CC} = 5\text{V}$	3.0	3.3		2.75	3.3		V
			100			100		nsec
Fall Time of Output			100			100		nsec

NOTES

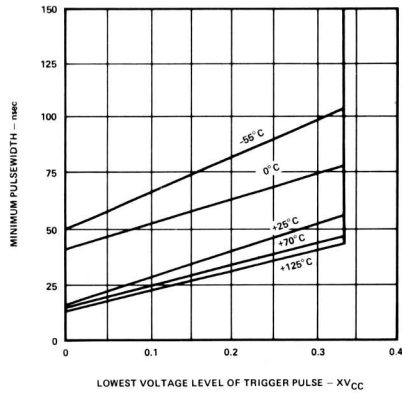
1. Supply Current when output high typically 1mA less.
2. Tested at $V_{CC} = 5\text{V}$ and $V_{CC} = 15\text{V}$
3. This will determine the maximum value of $R_A + R_B$. For 15V operation, the max total R = 20 megohm.

EQUIVALENT CIRCUIT (Shown for One Side Only)

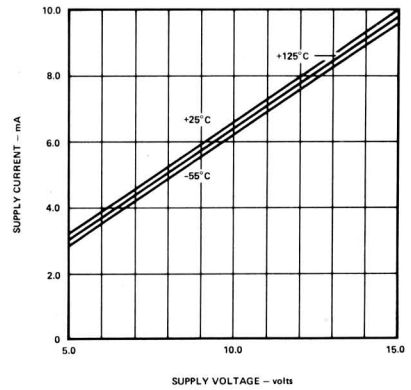


TYPICAL CHARACTERISTICS

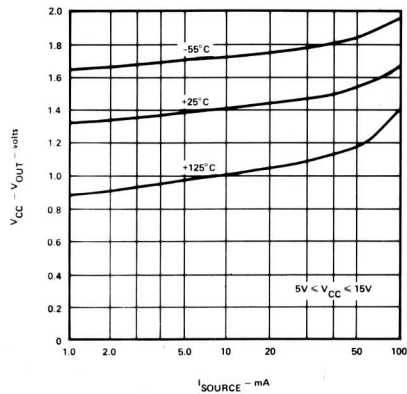
MINIMUM PULSE WIDTH
REQUIRED FOR TRIGGERING



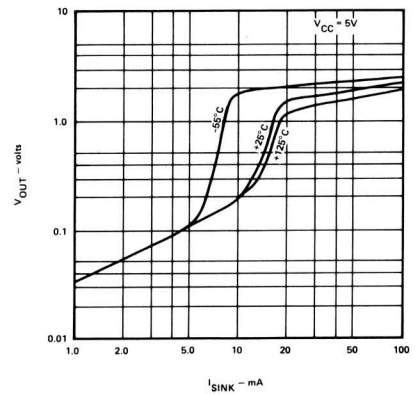
SUPPLY CURRENT
vs SUPPLY VOLTAGE



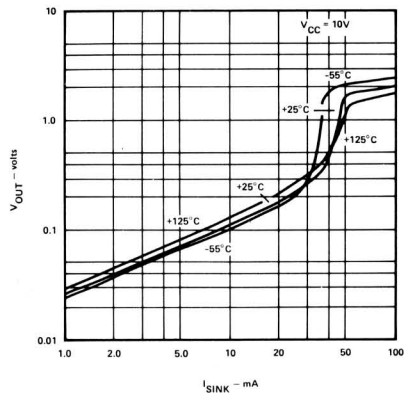
LOW OUTPUT VOLTAGE
vs OUTPUT SINK CURRENT



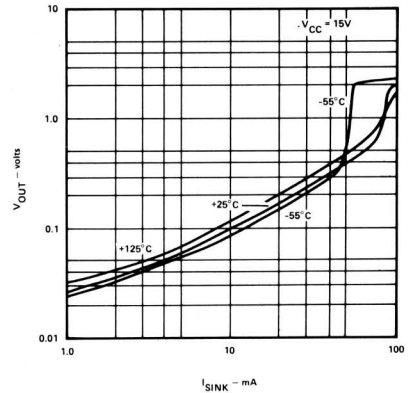
HIGH OUTPUT VOLTAGE
vs OUTPUT
SOURCE CURRENT



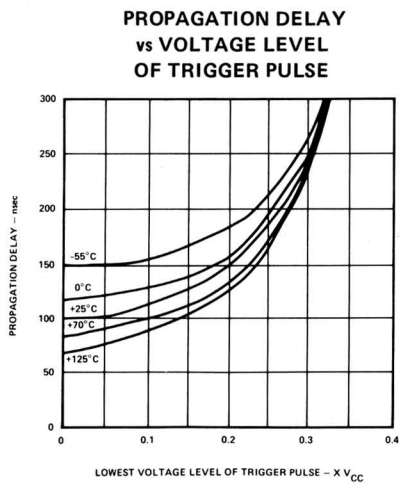
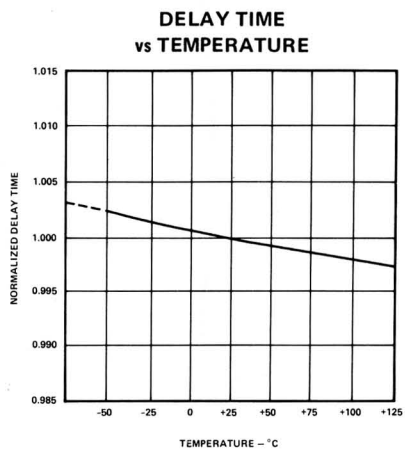
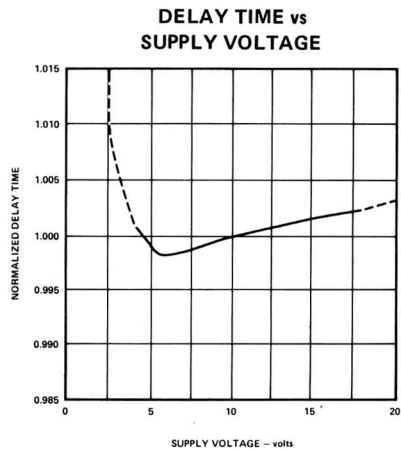
LOW OUTPUT VOLTAGE
vs OUTPUT SINK CURRENT



LOW OUTPUT VOLTAGE
vs OUTPUT SINK CURRENT



TYPICAL CHARACTERISTICS (Cont'd)



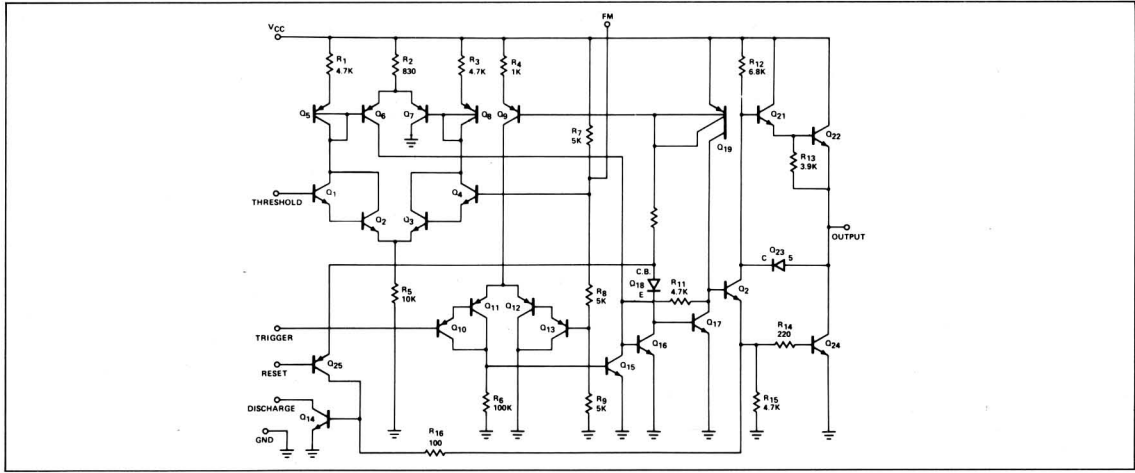
ELECTRICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, $V_{CC} = +5\text{V}$ to $+15$ unless otherwise specified

PARAMETER	TEST CONDITIONS	SE 556			NE 556			UNITS
		MIN	TYP	MAX	MIN	TYP	MAX	
Supply Voltage		4.5		18	4.5		16	V
Supply Current	$V_{CC} = 5\text{V}$ $R_L = \infty$		3	5		3	6	mA
	$V_{CC} = 15\text{V}$ $R_L = \infty$		10	11		10	14	mA
Timing Error (Monostable)	Low State, Note 1							
	$R_A = 2\text{K}\Omega$ to $100\text{K}\Omega$							
Initial Accuracy	$C = 0.1\mu\text{F}$ Note 2		0.5	1.5		0.75		%
Drift with Temperature			30	100		50		ppm/ $^\circ\text{C}$
Drift with Supply Voltage			0.05	0.2		0.1		%/Volt
Timing Error (Astable)	$R_A, R_B = 2\text{K}\Omega$ to $100\text{K}\Omega$							
Initial Accuracy	$C = 0.1\mu\text{F}$ Note 2		1.5			2.25		%
Drift with Temperature			90			150		ppm/ $^\circ\text{C}$
Drift with Supply Voltage			0.15			0.3		%/Volt
Threshold Voltage			2/3			2/3		$\times V_{CC}$
Threshold Current	Note 3		30	100		30	100	nA
Trigger Voltage	$V_{CC} = 15\text{V}$	4.8	5	5.2		5		V
	$V_{CC} = 5\text{V}$	1.45	1.67	1.9		1.67		V
Trigger Current			0.5			0.5		μA
Reset Voltage		0.4	0.7	1.0	0.4	0.7	1.0	V
Reset Current			0.1			0.1		mA
Control Voltage Level	$V_{CC} = 15\text{V}$	9.6	10	10.4	9.0	10	11	V
	$V_{CC} = 5\text{V}$	2.9	3.33	3.8	2.6	3.33	4	V
Output Voltage Drip (low)	$V_{CC} = 15\text{V}$							
	$I_{\text{SINK}} = 10\text{mA}$		0.1	0.15		0.1	.25	V
	$I_{\text{SINK}} = 50\text{mA}$		0.4	0.5		0.4	.75	V
	$I_{\text{SINK}} = 100\text{mA}$		2.0	2.25		2.0	2.75	V
	$I_{\text{SINK}} = 200\text{mA}$		2.5			2.5		V
	$V_{CC} = 5\text{V}$							
	$I_{\text{SINK}} = 8\text{mA}$		0.1	0.25				V
	$I_{\text{SINK}} = 5\text{mA}$					.25	.35	
Output Voltage Drop (high)	$I_{\text{SOURCE}} = 200\text{mA}$		12.5			12.5		
	$V_{CC} = 15\text{V}$							
	$I_{\text{SOURCE}} = 100\text{mA}$							
	$V_{CC} = 15\text{V}$	13.0	13.3		12.75	13.3		V
	$V_{CC} = 5\text{V}$	3.0	3.3		2.75	3.3		V
Rise Time of Output			100			100		nsec
Fall Time of Output			100			100		nsec
Discharge Leakage Current			20	100		20	100	nA
Matching Characteristics (Note 4)								
Initial Timing Accuracy			0.05	0.1		0.1	0.2	%
Timing Drift with Temperature			± 10			± 10		ppm/ $^\circ\text{C}$
Drift with Supply Voltage			0.1	0.2		0.2	0.5	%/Volt

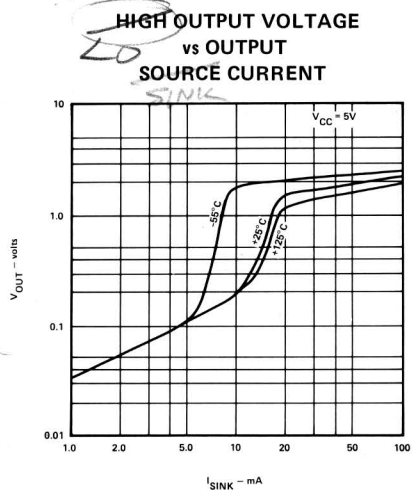
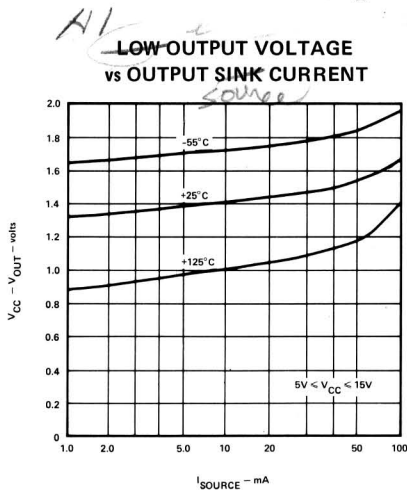
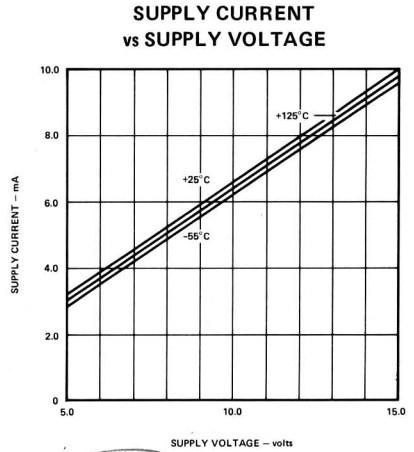
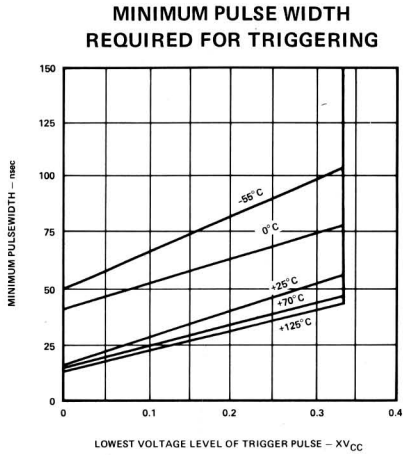
NOTES

1. Supply current when output is high is typically 1.0mA less.
2. Tested at $V_{CC} = 5\text{V}$ and $V_{CC} = 15\text{V}$.
3. This will determine the maximum value of $R_A + R_B$ for 15V operation. The maximum total $R = 20$ meg-ohms.
4. Matching characteristics refer to the difference between performance characteristics of each timer section.

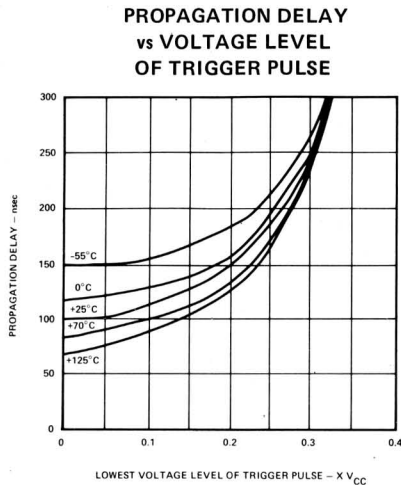
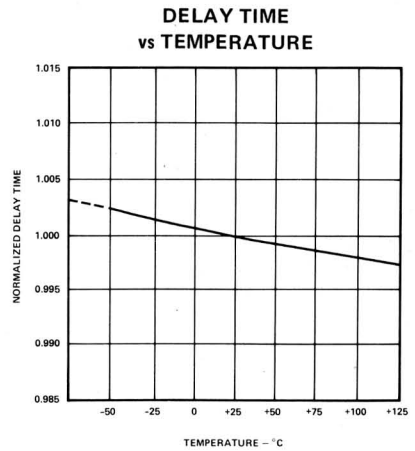
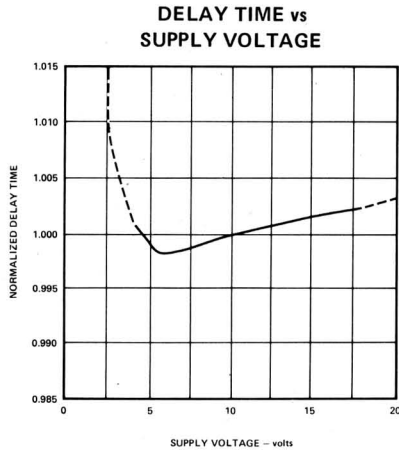
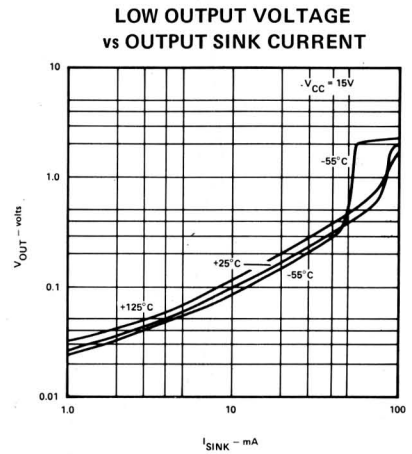
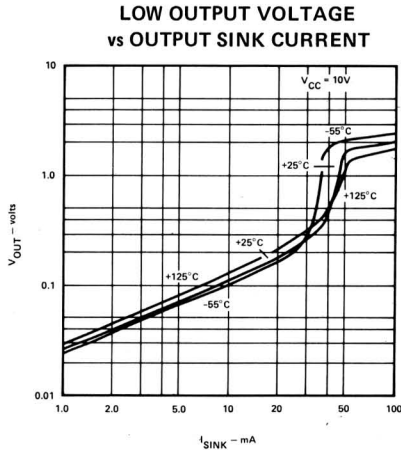
EQUIVALENT CIRCUIT (Shown for One Side Only)



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont'd)



APPLICATIONS INFORMATION

MONOSTABLE OPERATION

In this mode of operation, the timer functions as a one-shot. Referring to Figure 1a the external capacitor is initially held discharged by a transistor inside the timer.

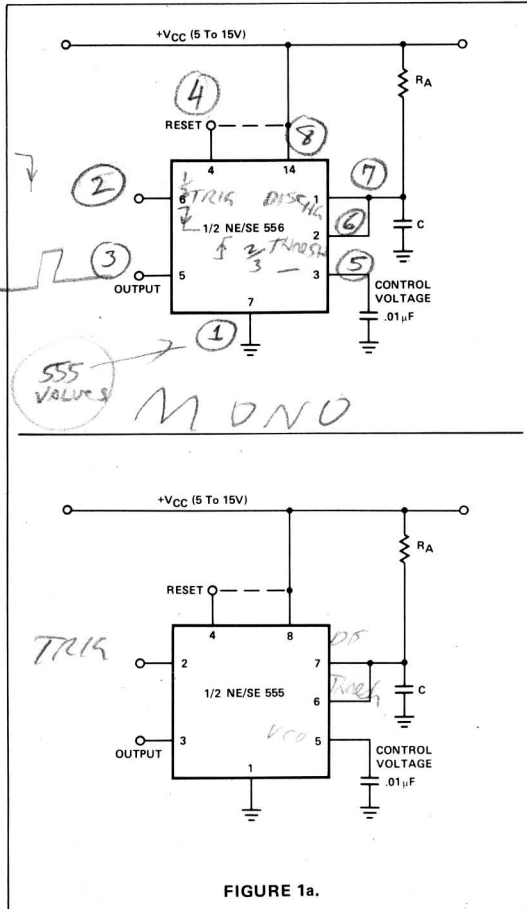


FIGURE 1a.

Upon application of a negative trigger pulse to pin 2, the flip-flop is set which releases the short circuit across the external capacitor and drives the output high. The voltage across the capacitor, now, increases exponentially with the time constant $\tau = R_A C$. When the voltage across the capacitor equals $2/3 V_{CC}$, the comparator resets the flip-flop which in turn discharges the capacitor rapidly and drives the output to its low state. Figure 1b shows the actual waveforms generated in this mode of operation.

The circuit triggers on a negative going input signal when the level reaches $1/3 V_{CC}$. Once triggered, the circuit will remain in this state until the set time is elapsed, even if it is triggered again during this interval. The time that the output is in the high state is given by $t = 1.1 R_A C$ and can easily be determined by Figure 1c. Notice that since the charge rate, and the threshold level of the comparator are both directly proportional to supply voltage, the timing

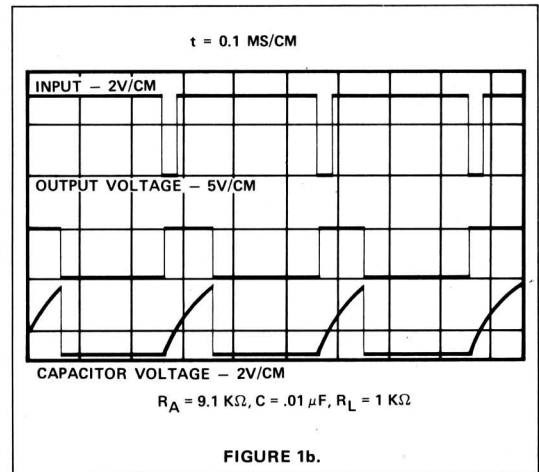


FIGURE 1b.

interval is independent of supply. Applying a negative pulse simultaneously to the reset terminal (pin 4) and the trigger terminal (pin 2) during the timing cycle discharges the external capacitor and causes the cycle to start over again. The timing cycle will now commence on the positive edge of the reset pulse. During the time the reset pulse is applied, the output is driven to its low state.

When the reset function is not in use, it is recommended that it be connected to V_{CC} to avoid any possibility of false triggering.

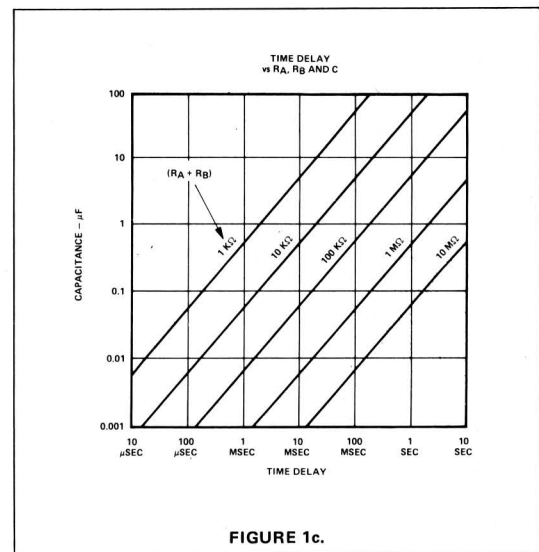


FIGURE 1c.

ASTABLE OPERATION

If the circuit is connected as shown in Figure 2a (pins 2 and 6 connected) it will trigger itself and free run as a multi-vibrator. The external capacitor charges through R_A and R_B and discharges through R_B only. Thus the duty cycle may be precisely set by the ratio of these two resistors.

APPLICATIONS INFORMATION (Cont'd)

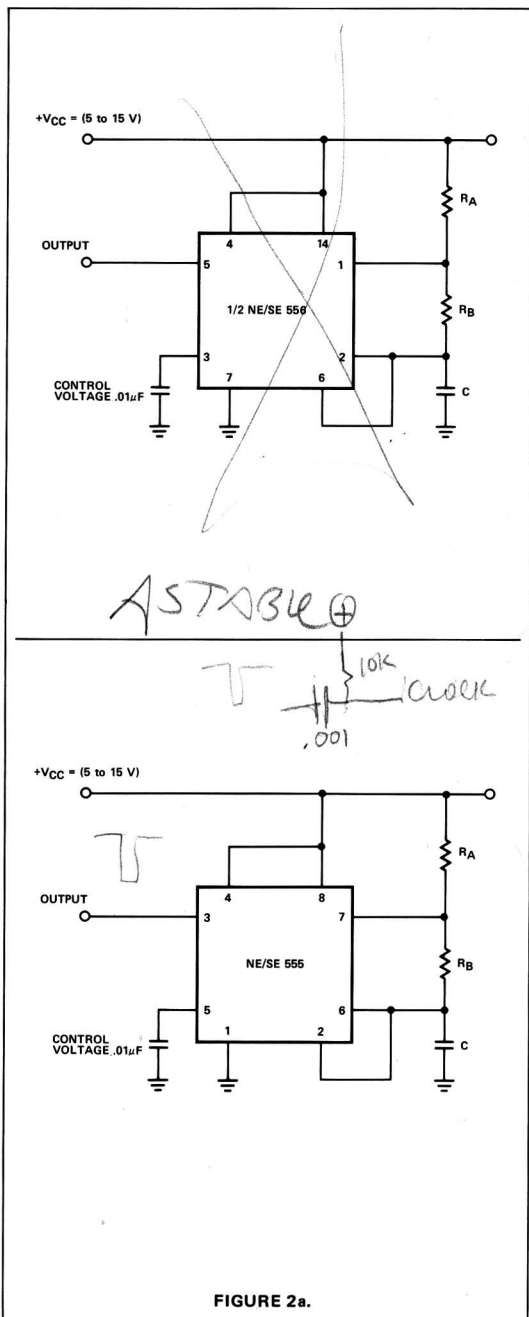


FIGURE 2a.

In this mode of operation, the capacitor charges and discharges between $1/3 V_{CC}$ and $2/3 V_{CC}$. As in the triggered mode, the charge and discharge times, and therefore the frequency are independent of the supply voltage.

Figure 2b shows actual waveforms generated in this mode of operation.

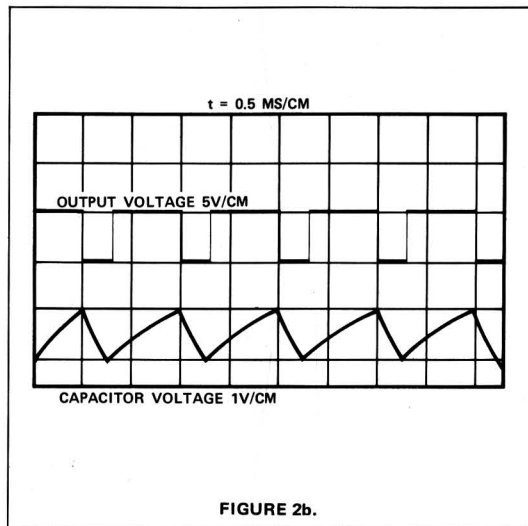


FIGURE 2b.

The charge time (output high) is given by:

$$t_1 = 0.693 (R_A + R_B) C$$

and the discharge time (output low) by:

$$t_2 = 0.693 (R_B) C$$

Thus the total period is given by:

$$T = t_1 + t_2 = 0.693 (R_A + 2R_B) C$$

The frequency of oscillation is then:

$$f = \frac{1}{T} = \frac{1.44}{(R_A + 2R_B) C}$$

and may be easily found by Figure 2c.

The duty cycle is given by:

$$D = \frac{R_B}{R_A + 2R_B}$$

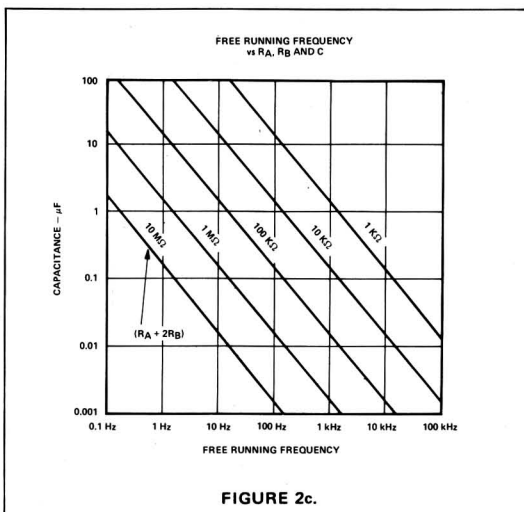
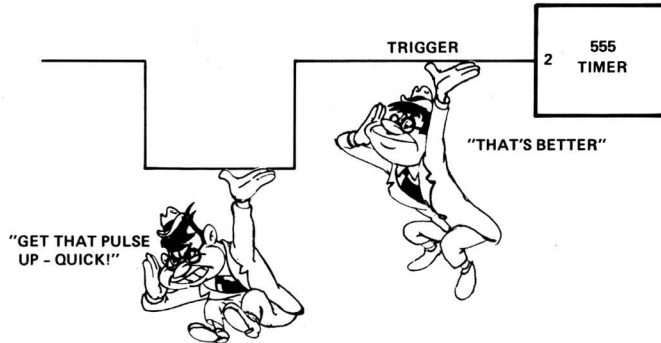


FIGURE 2c.

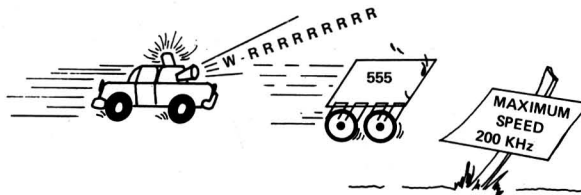
TRIGGER MODE



THE DEVICE TRIGGERS ON THE NEGATIVE GOING EDGE OF A LOW GOING PULSE. THE TRIGGER PULSE MUST BE OF SHORTER DURATION THAN THE "RC" TIME INTERVAL. IF THE TRIGGER IS HELD LOW, THE OUTPUT WILL STAY HIGH UNTIL TRIGGER IS DRIVEN HIGH AGAIN.

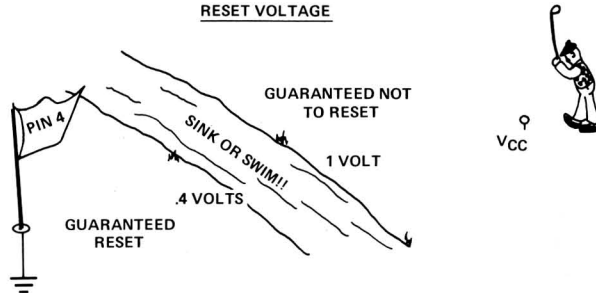
MAXIMUM OSCILLATION FREQUENCY

10K
 .000100pf
 10K
 .01
 .001
 .009010
 10µsec



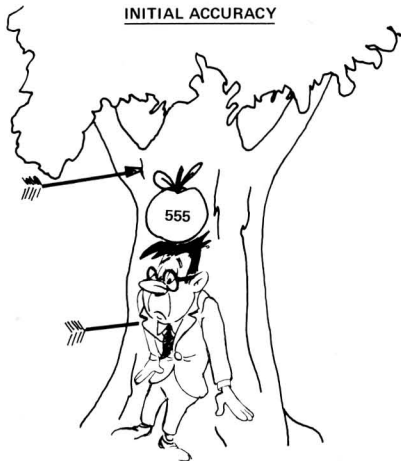
THE 555 TIMER IS CAPABLE OF OSCILLATING AT UP TO 300 KHz. HOWEVER, FOR TEMPERATURE STABILITY THE LIMIT SHOULD BE AROUND 200 KHz.

RESET VOLTAGE



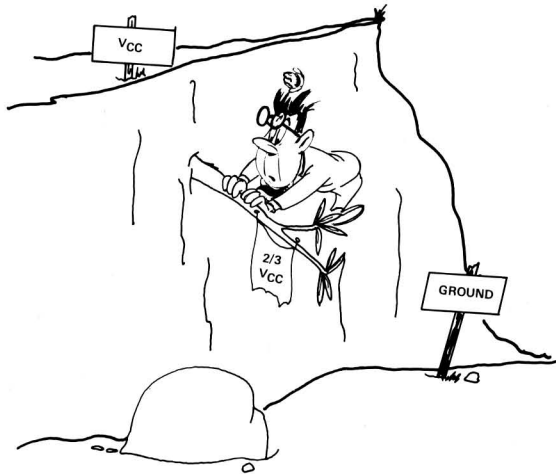
THE RESET ACTS AS AN INHIBIT. WHEN THE RESET (PIN 4) IS ABOVE 1 VOLT THE DEVICE IS FREE TO FUNCTION. IF THE RESET IS TAKEN BELOW .4 VOLTS, THE OUTPUT IS FORCED LOW. WHEN THE RESET IS RELEASED, THE OUTPUT WILL STILL REMAIN LOW UNTIL A TRIGGER PULSE IS APPLIED.

INITIAL ACCURACY



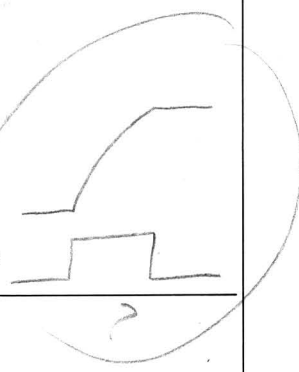
THE INITIAL ACCURACY IS THE TIMING REPEATABILITY FROM DEVICE TO DEVICE AND ALSO THE SAME DEVICE TODAY, TOMORROW AND 3 YEARS FROM NOW, WITH THE SAME "RC" NETWORK AND SUPPLY VOLTAGE. TYPICALLY, THE NE555 HAS A 1% INITIAL ACCURACY.

THRESHOLD VOLTAGE

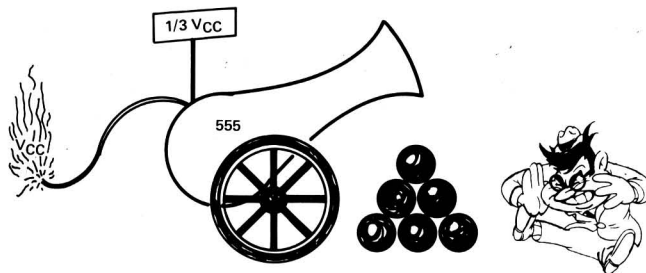


WHEN TRIGGERED, THE TIMER STARTS ITS TIMING CYCLE BY DRIVING THE OUTPUT, PIN 3, HIGH. SIMULTANEOUSLY, THE TIMING CAPACITOR STARTS CHARGING FROM ITS STEADY STATE LEVEL AT GROUND. WHEN IT REACHES $2/3 V_{CC}$, AN INTERNAL COMPARATOR IS TRIPPED, CAUSING THE CAPACITOR TO DISCHARGE TO GROUND. THIS DRIVES THE OUTPUT LOW, ENDING THE TIMING CYCLE.

NOT CORRECTION?

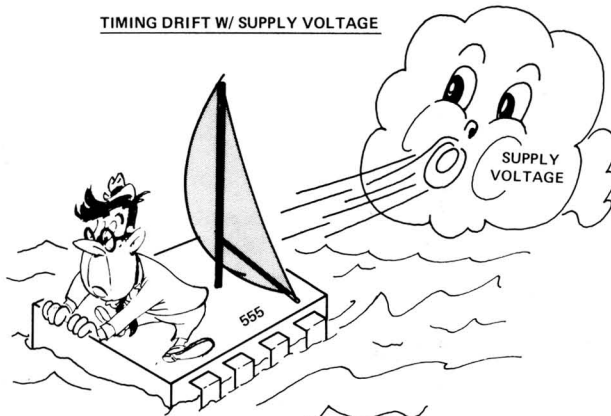


TRIGGER VOLTAGE



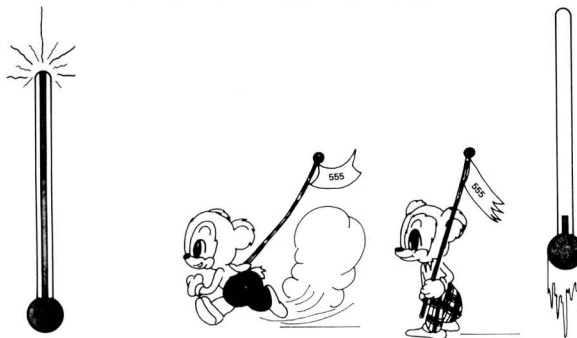
THE TRIGGER PULSE MUST DROP BELOW $1/3$ OF THE SUPPLY VOLTAGE BEFORE THE TIMER TRIGGERS.

TIMING DRIFT W/ SUPPLY VOLTAGE



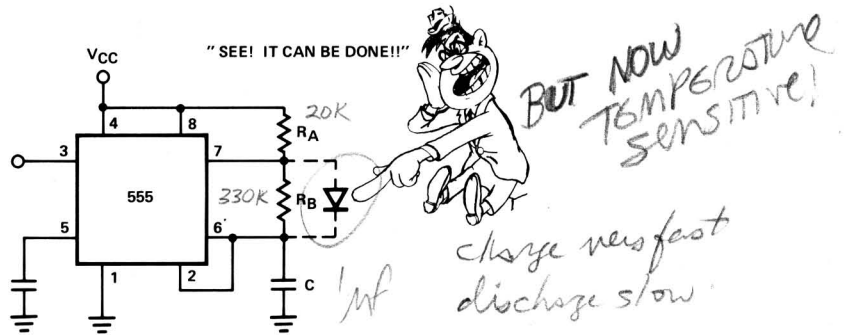
THE TIMING OF THE DEVICE WILL VARY SLIGHTLY WITH CHANGE IN SUPPLY VOLTAGE. THE TYPICAL TIMING DRIFT IS .01% PER VOLT.

TIMING DRIFT W/ TEMPERATURE



THE TIMER IN THE MONOSTABLE MODE HAS A TIMING DRIFT OF 50 PPM/ $^{\circ}$ C TYPICAL. IN THE ASTABLE MODE, SINCE BOTH COMPARATORS OF THE DEVICE ARE USED, THE DRIFT IS SOMEWHAT GREATER. TYPICALLY 150 PPM/ $^{\circ}$ C DRIFT.

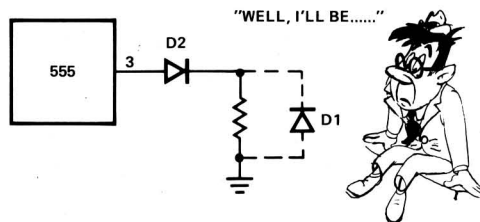
DUTY CYCLE



THE DUTY CYCLE IS "ON TIME" EXPRESS IN TERMS OF TOTAL CYCLE TIME. THE DUTY CYCLE IS LIMITED, UNDER NORMAL CIRCUMSTANCES, TO 50%. HOWEVER, BY ADDING A DIODE A DUTY CYCLE OF LESS THAN 50% CAN BE ACHIEVED.



LATCH UP WHEN DRIVING AN INDUCTIVE LOAD



A NEGATIVE VOLTAGE AT PIN 3 CAN CAUSE A LATCH UP. THE SOLUTION IS TO ADD TWO DIODES AS SHOWN. THIS CIRCUIT PROHIBITS A NEGATIVE VOLTAGE FROM REACHING PIN 3.

CONTROL VOLTAGE



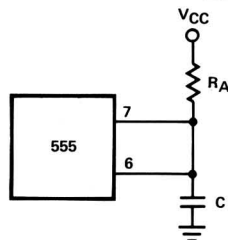
PIN 5, THE CONTROL VOLTAGE PIN, IS PRIMARILY USED FOR FILTERING WHEN DEVICE IS USED IN NOISY ENVIRONS. HOWEVER, BY IMPOSING A VOLTAGE AT THIS POINT, IT IS POSSIBLE TO VARY THE TIMING OF THE DEVICE INDEPENDENTLY OF THE "RC" NETWORK. THE CONTROL VOLTAGE MAY BE VARIED FROM 45% TO 90% OF V_{CC} IN THE MONOSTABLE MODE, AND FROM 1.7 VOLTS TO V_{CC} IN THE ASTABLE MODE.

2.6-4V @ 5V_{CC}

FORMULAS

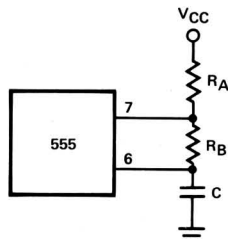


MONOSTABLE TIMING



$$T(\text{OUTPUT HIGH}) = 1.1 R_A C$$

ASTABLE TIMING



$$t_1(\text{OUTPUT HIGH}) = 0.693 (R_A + R_B) C$$

$$t_2(\text{OUTPUT LOW}) = 0.693 (R_B) C$$

$$T = t_1 + t_2 \text{ (TOTAL PERIOD)}$$

$$f = \frac{1}{T} = \frac{1.44}{(R_A + 2R_B) C}$$

$$D(\text{DUTY CYCLE}) = \frac{R_B}{R_A + 2R_B}$$

HERE ARE SOME ADDITIONAL INGENUOUS APPLICATIONS DEvised BY SIGNETICS ENGINEERS AND SOME OF OUR CUSTOMERS.

MISSING PULSE DETECTOR

Using the circuit of Figure 3a, the timing cycle is continuously reset by the input pulse train. A change in frequency, or a missing pulse, allows completion of the timing cycle which causes a change in the output level. For this application, the time delay should be set to be slightly longer than the normal time between pulses. Figure 3b shows the actual waveforms seen in this mode of operation.

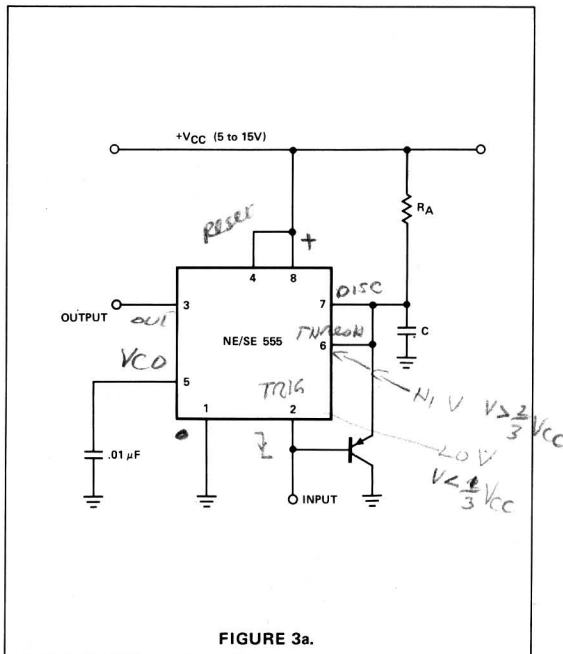


FIGURE 3a.

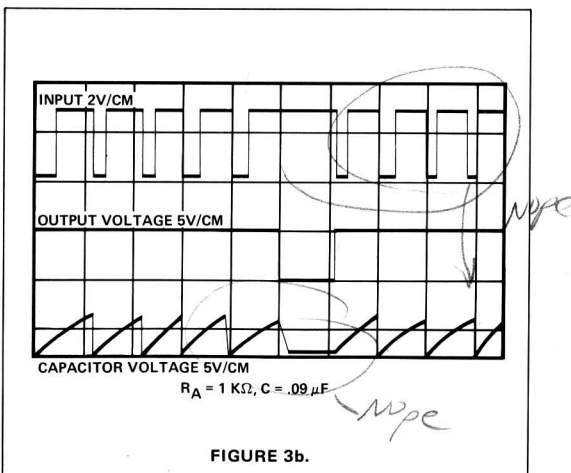


FIGURE 3b.

FREQUENCY DIVIDER

If the input frequency is known, the timer can easily be used as a frequency divider by adjusting the length of the timing cycle. Figure 4 shows the waveforms of the timer in Figure 1a when used as a divide by three circuit. This application makes use of the fact that this circuit cannot be retrigged during the timing cycle.

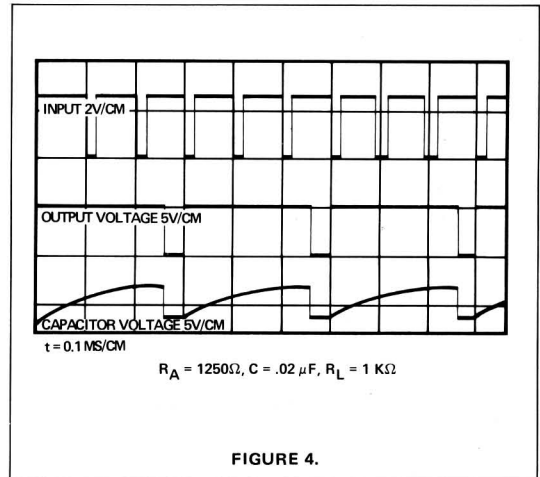


FIGURE 4.

PULSE WIDTH MODULATION (PWM)

In this application, the timer is connected in the mono-stable mode as shown in Figure 5a. The circuit is triggered with a continuous pulse train and the threshold voltage is

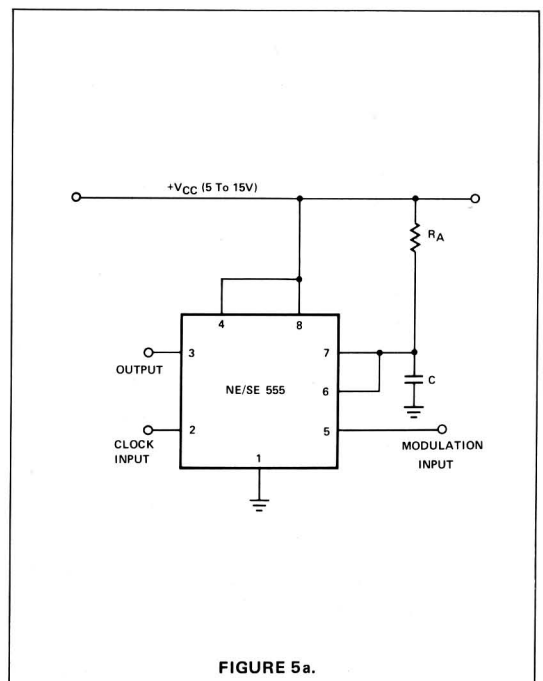


FIGURE 5a.

APPLICATIONS INFORMATION (Cont'd)

modulated by the signal applied to the control voltage terminal (pin 5). This has the effect of modulating the pulse width as the control voltage varies. Figure 5b shows the actual waveforms generated with this circuit.

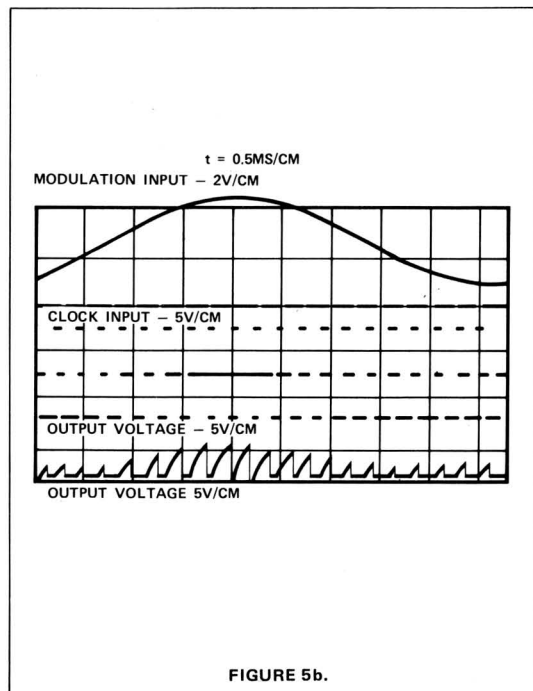


FIGURE 5b.

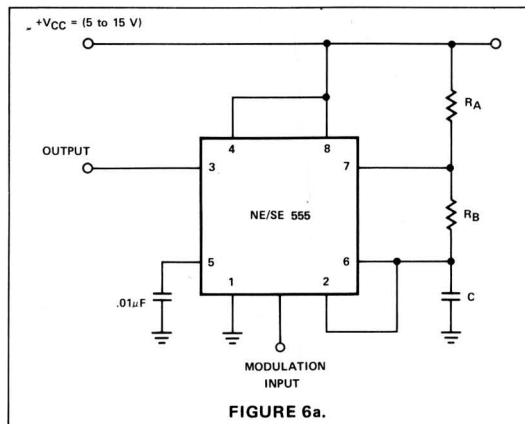


FIGURE 6a.

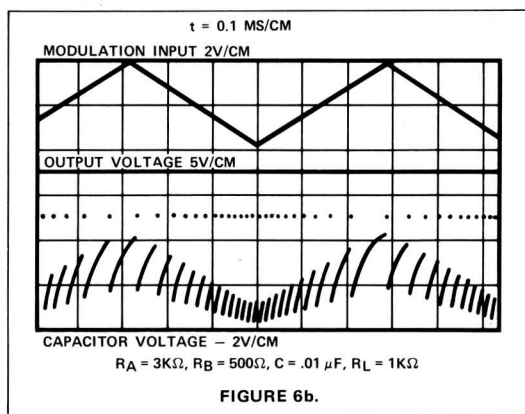


FIGURE 6b.

PULSE POSITION MODULATION (PPM)

This application uses the timer connected for astable (free-running) operation, Figure 6a, with a modulating signal again applied to the control voltage terminal. Now the pulse position varies with the modulating signal, since the threshold voltage and hence the time delay is varied. Figure 6b shows the waveforms generated for triangle wave modulation signal.

TEST SEQUENCER

Figure 7 shows several timers connected sequentially. The first timer is started by momentarily connecting pin 2 to ground, and runs for 10 msec. At the end of its timing cycle, it triggers the second circuit which runs for 50 msec. After this time, the third circuit is triggered. Note that the timing resistors and capacitors can be programmed digitally and that each circuit could easily trigger several other timers to start concurrent sequences.

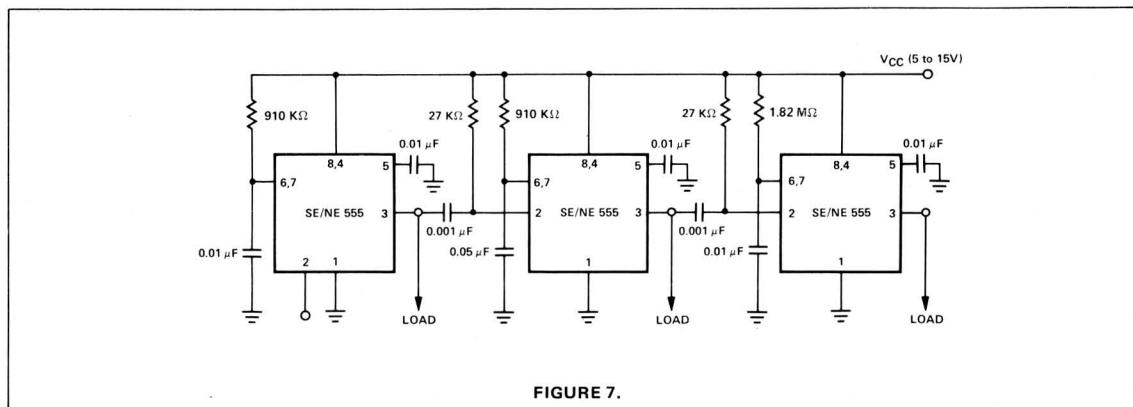


FIGURE 7.

APPLICATIONS INFORMATION

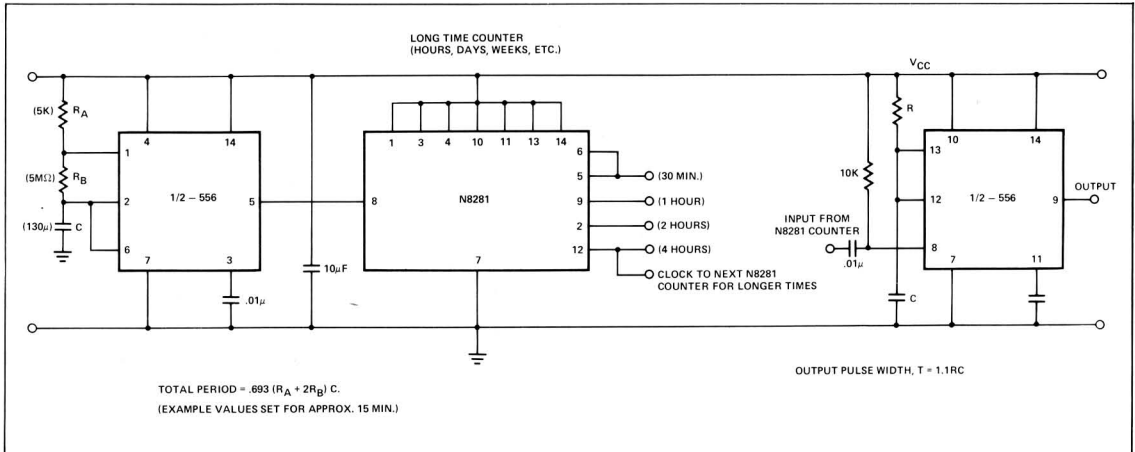
Each half of the 556 behaves like a separate 555 timer and as such all of the applications indicated in the Data Sheet for the 555 also are applicable to the 556.

LONG TIME DELAYS

In the 556 timer the timing is a function of the charging rate of the external capacitor. For long time delays expensive capacitors with extremely low leakage are required. The practicality of the components involved limits the time between pulses to something in the neighborhood of ten minutes.

To achieve longer time periods both halves may be connected in tandem with a "Divide-by" network in between the first timer section operates in an oscillatory mode with a period of $1/f_0$.

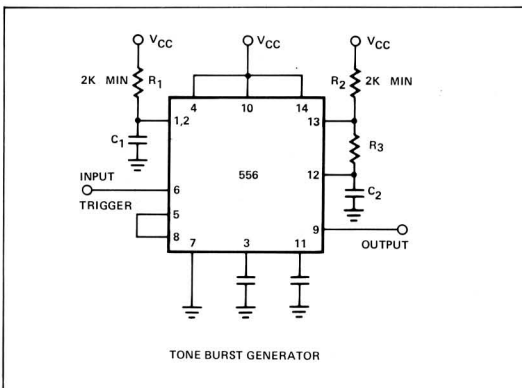
This signal is then applied to a "Divide-by-N" network to give an output with the period of N/f_0 . This can then be used to trigger the second half of the 556. The total time delay is now a function of N and f_0 .



TONE BURST GENERATOR

The 556 Dual Timer makes an excellent Tone Burst Generator. The first half is connected as a one shot and the second half as an oscillator.

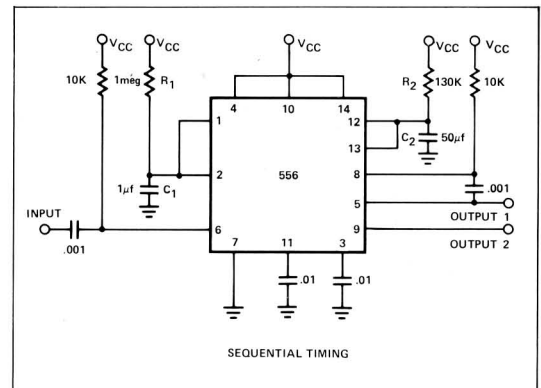
necting the output of the first half to the input of the second half via a $.001\mu\text{fd}$ coupling capacitor sequential timing may be obtained. Delay t_1 is determined by the first half and t_2 by the second half delay.



The pulse established by the one shot turns on the oscillator allowing a burst of pulses to be generated.

SEQUENTIAL TIMING

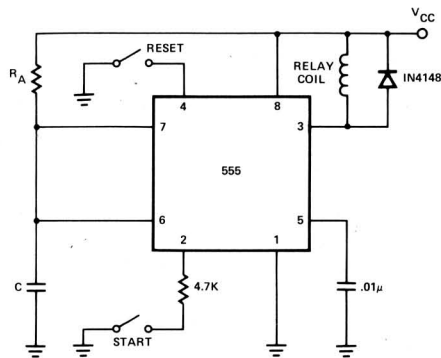
One feature of the Dual Timer is that by utilizing both halves it is possible to obtain sequential timing. By con-



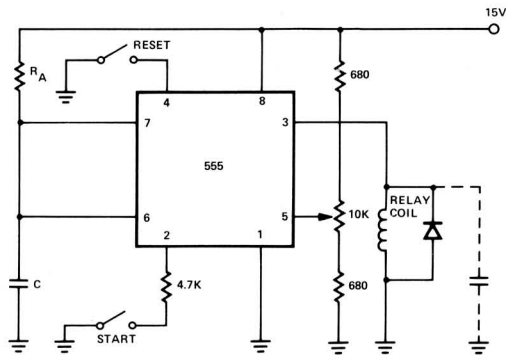
The first half of the timer is started by momentarily connecting pin 6 to ground. When it is timed out (determined by $1.1R_1C_1$) the second half begins. Its time duration is determined by $1.1R_2C_2$.

APPLICATIONS

SIMPLE TIME DELAY

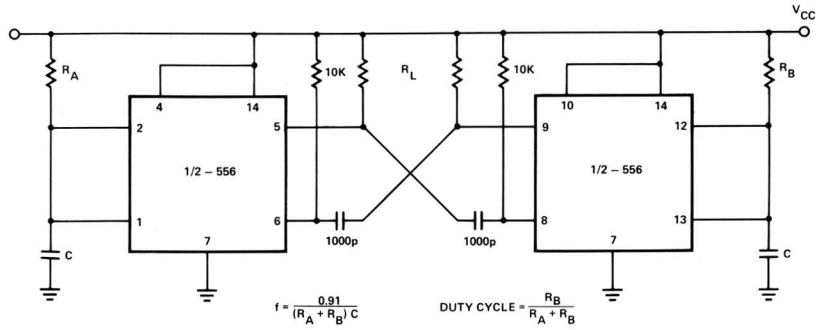


SIMPLE TIME DELAY

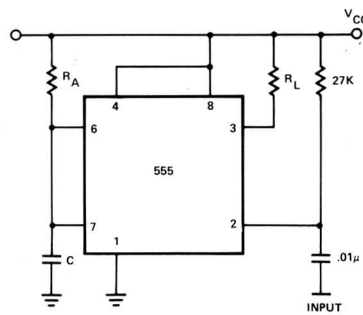


APPLICATIONS (Cont'd)

DUAL ASTABLE

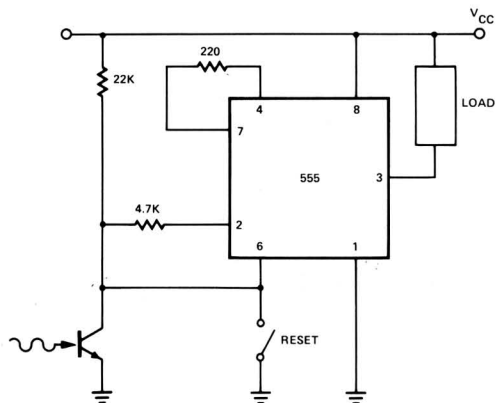


TOUCH CONTROL

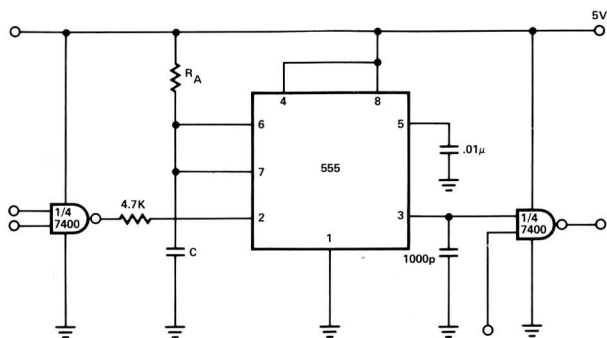


APPLICATIONS (Cont'd)

BURGLAR ALARM

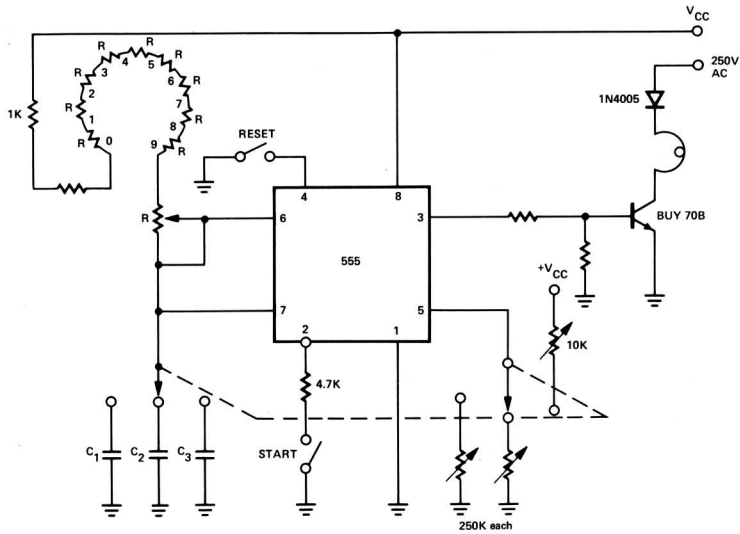


TTL MONOSTABLE

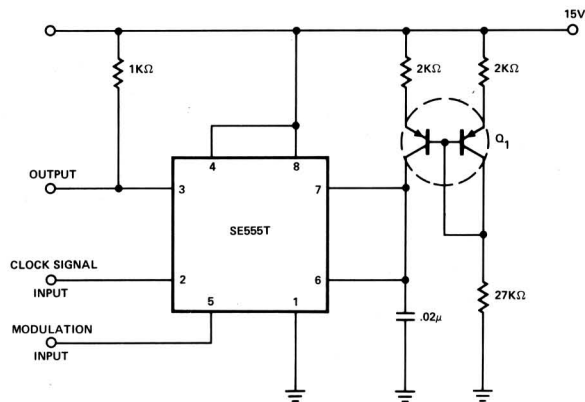


APPLICATIONS (Cont'd)

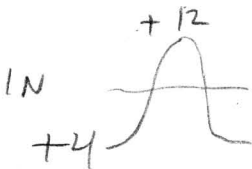
PHOTOGRAPHIC TIMER



LINEAR PULSE WIDTH MODULATOR

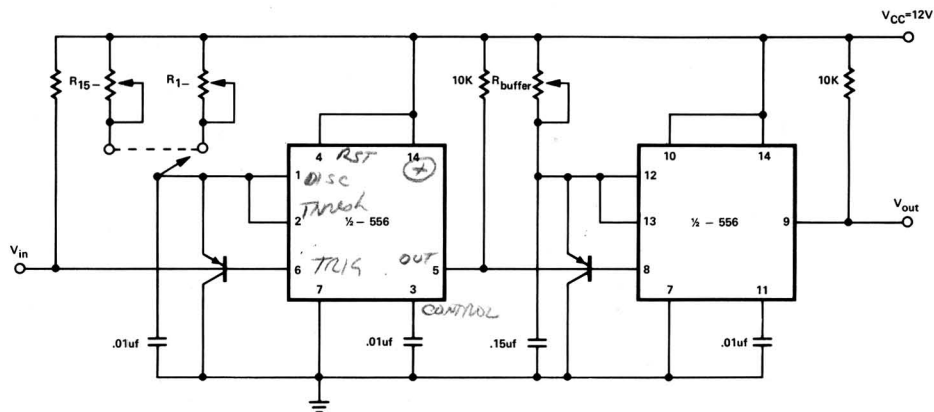


LINEARITY, $\pm 0.2\%$ over +4 to +12 volt input

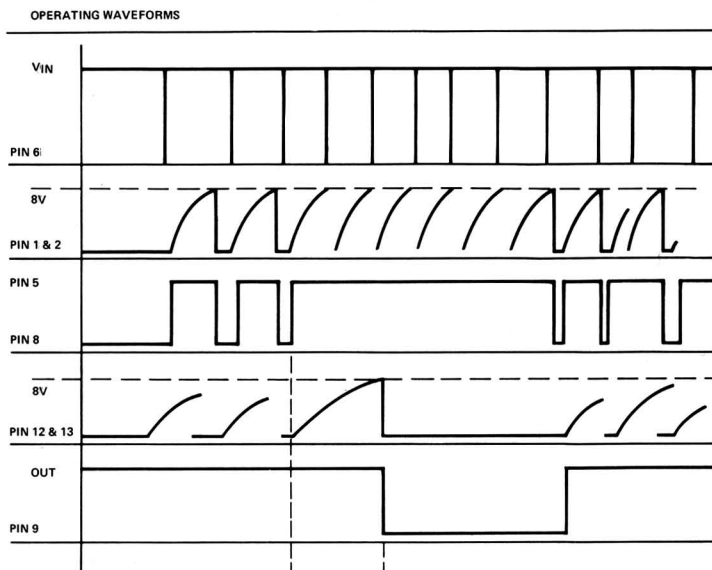


APPLICATIONS (Cont'd)

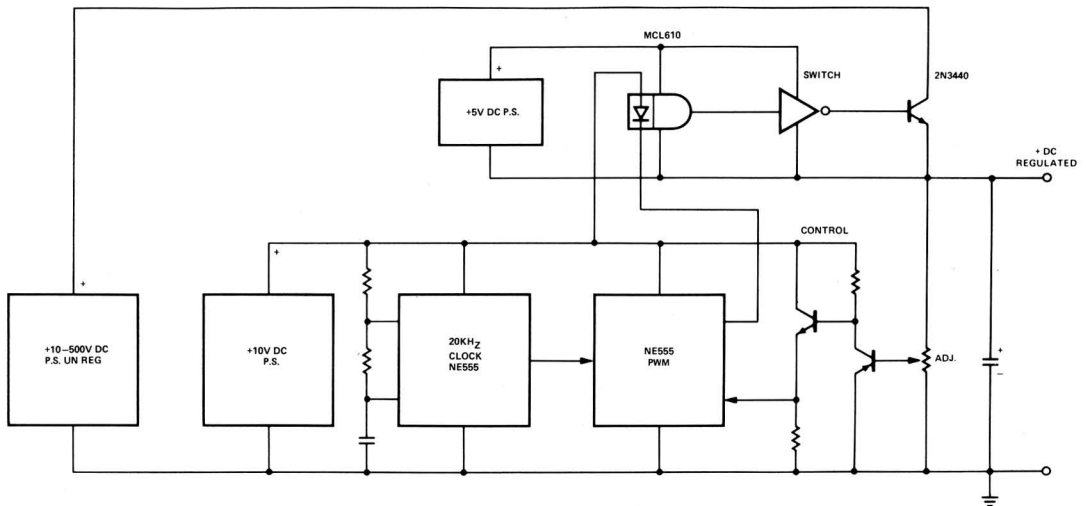
SPEED WARNING DEVICE



OPERATING WAVEFORMS

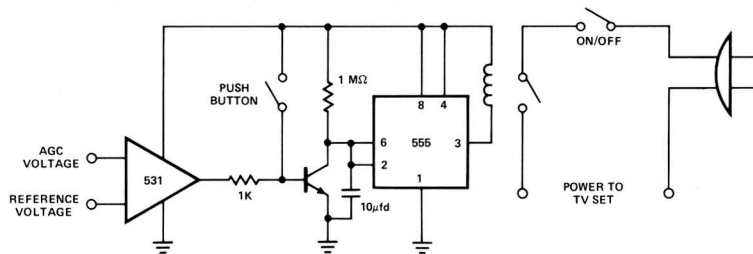


REMOTE CONTROLLED DC SWITCHING REGULATOR



APPLICATION—A PULSE WIDTH MODULATOR TO FEED DIGITAL PULSES INTO SWITCHING SECTION OF REGULATOR PROPORTIONAL TO ERROR SIGNAL. ADJUST POT CAN BE REMOTELY POSITIONED.

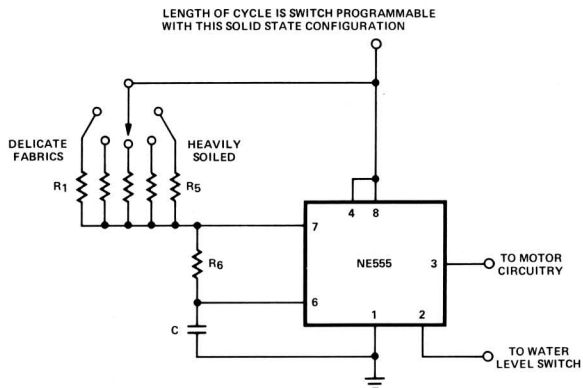
AUTOMATIC TURN OFF FOR TV SET



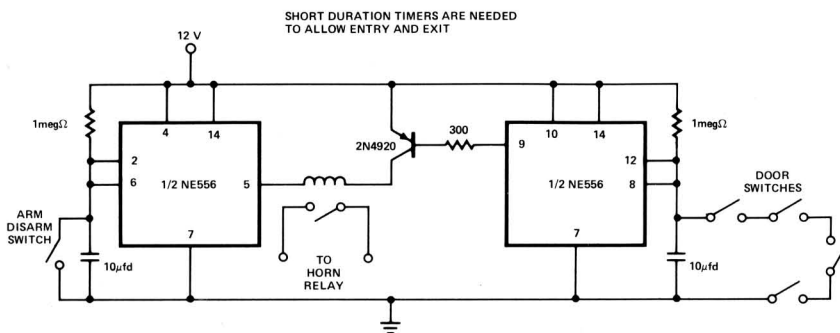
SET TURNS OFF SHORTLY AFTER TV STATION STOPS BROADCASTING

APPLICATIONS (Cont'd)

WASHER TIMER

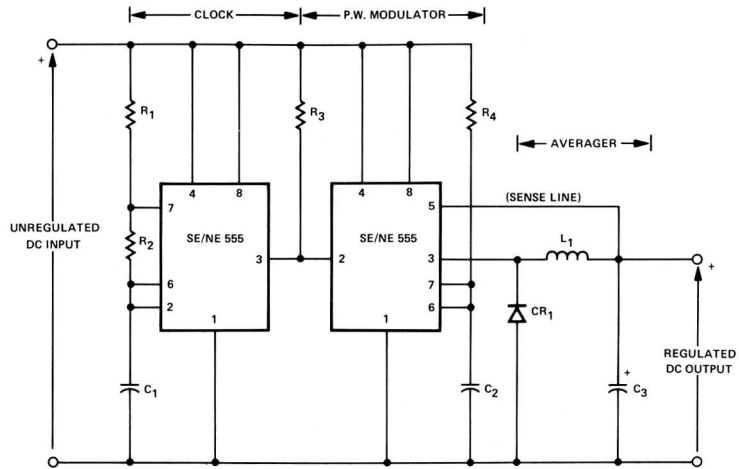


AUTO BURGLAR ALARM

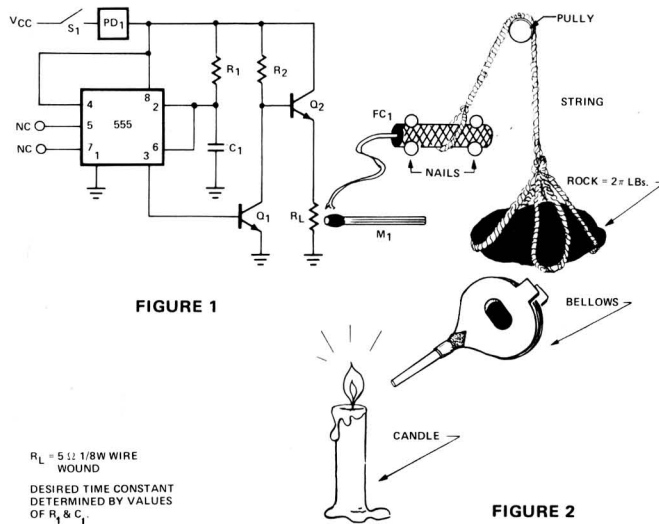


APPLICATIONS (Cont'd)

SWITCHING STEP-DOWN REGULATOR



SCHEMATIC DIAGRAM OF DELAYED LIGHT TURN-OFF



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Hollywood 33021: Hamilton/Avnet Electronics, 4020 No. 29th Ave.
Phone: (305) 925-5401 TELEX: 51-4328

Hollywood 33020: Schweber Electronics, 2830 North 28th Terrace
Phone: (305) 927-0511

Orlando 32805: Hammond Electronics, 911 West Central Blvd.
Phone: (305) 241-6601 TWX: (810) 850-4121

GEORGIA

Atlanta 30340: Schweber Electronics,
4126 Pleasantdale Road, Suite 14
Phone: (404) 449-9170

Norcross 30071: Hamilton/Avnet Electronics
6700 Interstate 85 Access Road, Suite 2B
Phone: (404) 448-0800 TELEX: 542-127

ILLINOIS

Elk Grove 60007: Schweber Electronics, 1380 Jarvis Avenue
Phone: (312) 593-2740

Elmhurst 60126: Semiconductor Specialists, Inc.,
195 Spangler Avenue, Elmhurst Industrial Park
Phone: (312) 279-1000 TWX: 254-0169

Schiller Park 60176: Hamilton/Avnet Electronics,
3901 No. 25th Avenue
Phone: (312) 678-6310 TELEX: 728-330

INDIANA

Indianapolis 46241: Semiconductor Specialists,
P.O. Box 41630, Weir Cook Airport
Phone: (317) 243-8271 TWX: (810) 341-3126

KANSAS

Lenexa 66215: Hamilton/Avnet Electronics,
37 Lenexa Industrial Center
Phone: (913) 888-8900

MARYLAND

Hanover 21076: Hamilton/Avnet Electronics, 7255 Standard Drive,
Phone: (301) 796-5000 TELEX: 879-68

Rockville 20850: Pioneer Washington Electronics, Inc.,
1037 Taft Street Phone: (301) 424-3300

Rockville 20852: Schweber Electronics, 5640 Fisher Lane
Phone: (301) 881-2970

MASSACHUSETTS

Burlington 01803: Hamilton/Avnet Electronics
185 Cambridge Street
Phone: (617) 273-2120 TELEX: 9496-61

Needham Heights 02194: Kierulff/Schley, 14 Charles Street
Phone: (617) 449-3600 TWX: (710) 326-1179

Newton 02150: Cramer Electronics, 85 Wells Avenue
Phone: (617) 969-7700

Waltham 02154: Schweber Electronics, 213 Third Ave.,
Phone: (617) 890-8484

WEST GERMANY

EBV Elektronik GmbH, Augustenstrasse 79, D 8 München 2
Phone: (0811) 52-43-40/48 TELEX: 524535

EBV Elektronik GmbH, Myliusstrasse 54 D 6 Frankfurt/Main 1
Phone: (0611) 72-04-16/18 TELEX: 413590

EBV Elektronik GmbH, Scheurenstrasse 1, D 4 Düsseldorf
Phone: (0211) 8-48-46/47 TELEX: 8587267

Mirotronic, 2 Hamburg 54, Ahornlee 5
Phone: (0411) 560-40-54 TELEX: 02 15427

EBV Elektronik GmbH, Abraham-Wolf Strasse 48,
D 7 Stuttgart-Sonnenberg
Phone: (0711) 765072/3 TELEX: 07255811

"Mütron" Muller & Co. KG, Postfach 164, Bornstrasse 22,
D 28 Bremen 1
Phone: (0421) 31-04-85 TELEX: 245-325

Omni-Ray GmbH, 67 Ludwigshafen, Moltkestrasse,
8 Postfach 211023
Phone: (0621) 51-30-55/56 TELEX: 0464557

Distrion GmbH, D 1 Berlin 31, Wilhelmsaue 39-41
Phone: (0311) 82-13-064 TELEX: 18-27-58

AUSTRIA

Ing. Ernst Steiner, Beckgasse 30, A-1130 Wien
Phone: (222) 82-10-605

SWITZERLAND

Omni Ray AG, Dufourstrasse 56, 8008 Zurich
Phone: 01/478200 TELEX: 53239

FRANCE

S. A. Gallec Electronique, 22 Rue Raymond Losserand,
75-Paris 14e Phone: 734-25-65/273-18-51

R.T.F., 73 Avenue Charles de Gaulle, 92-Neuilly
Phone: 722-70-40 TELEX: 65933

Elic 38, le Bureau Barisien S.A.R.L.,
8-10 Avenue du Grand Sablon, 38 La Tronche
Phone: (76) 87-67-71 TELEX: 32-739

ITALY

Metroelettronica S.A.S., Viale Cirene 18, 1-20135 Milano
Phone: 546-26-41 TELEX: 33-168 Metronix

ISRAEL

Rapac Electronics Ltd., 15 Karl Herbst St.,
Tel-Baruch, Tel-Aviv Phone: 77 71 15,6,7 TELEX: TV 528

ITALY

Metroelettronica S.A.S., Viale Cirene 18, 1-20135 Milano
Phone: 546-26-41 TELEX: 33-168 Metronix

UNITED KINGDOM

Quarndon Electronics Ltd., Slack Lane, Derby, Derbyshire
Phone: Derby 3 26 51 TELEX: 37163

S.D.S. (Components) Ltd., Hilsea Industrial Estate
Portsmouth, Hampshire
Phone: 6 53 11 TELEX: 86114

Semicomps Ltd., 5 Northfield Industrial Estate,
Beresford Ave., Wembley, Middlesex
Phone: (01) 903-3161 TELEX: 935243

A. M. Lock & Co. Ltd., Neville Street, Middleton Road,
Oldham, Lancashire
Phone: (061) 624-6832 TELEX: 669971

SCOTLAND

Semicomps Northern Ltd., 42 The Square, Kelso, Roxburghshire
Phone: Kelso 2366 TELEX: 72692

SWEDEN, NORWAY, FINLAND

A. B. Kuno Källman, Järnorget 7, S-413 04 Gothenburg, Sweden
Phone: 17-01-20 TELEX: 21072

A. B. Kuno Källman, Sibyllegarten 28, 11443, Stockholm, Sweden
Phone: 08/671711-671595 TELEX: 17165

DENMARK

E. Friis-Mikkelsen A/S, Krogshøjvej 51, DK-2880 Bagsvaerd
Phone: (01) 986333 TELEX: 2350

THE NETHERLANDS

Mulder-Hardenberg, Westerhoutpark 1-A, P. O. Box 3059, Haarlem
Phone: (023) 3191 84 TELEX: 41431

JAPAN

Asahi Glass Co., Ltd., 1-2 Marunouchi, 2 Chome Chiyoda-ku, Tokyo
Phone: 211-0411 TELEX: 24616

AFRICA

Allied Electronic (PTY) Ltd., P. O. Box 6090,
Dunswart, Transvaal, South Africa
Phone: 52431 TELEX: 43-7823

REPRESENTATIVES

SWEDEN, NORWAY, FINLAND

A. B. Kuno Källman, Järnorget 7, S-41304 Gothenburg, Sweden
Phone: 17-01-20 TELEX: 21072

A. B. Kuno Källman, Sibyllegarten 28, 11443, Stockholm, Sweden
Phone: 08/671711-671595 TELEX: 17165

AUSTRALIA

Pye Industries, Ltd., Tecnico Electronics Division,
Premier St., Marrickville, Sydney, N.S.W. 2204
Phone: 55-0411 TELEX: AA-21490

Pye Industries, Ltd., Tecnico Electronics Division,
2 High St., Northcote, Melbourne, Vic. 3070
Phone: 489-9322 TELEX AA-31240

ISRAEL

RAPAC Electronics Ltd., 15 Carl Herbst Street, Tel Baruch, Tel Aviv
Phone: 777115.6.7 TELEX: TV 528

JAPAN

Asahi Glass Co. Ltd., 1-2, Marunouchi, 2 Chome, Chiyoda-ku, Tokyo
Phone: 211-0411 TELEX: 24616

SWITZERLAND

Omni Ray AG, Dufourstrasse 56, 8008 Zurich
Phone: 01/478200 TELEX: 53239

INDIA

Semiconductors Limited, Nagar Rd., Mile 4/5, Ramawadi,
Poona 14, Maharashtra
Phone: 25186 CABLE: Transducer

signetics

**811 EAST ARQUES AVENUE
SUNNYVALE, CALIFORNIA 94086**

TEL.: (408) 739-7700

TWX: (910) 339-9283